

Devajeet Kar

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Profile

Innovative Software Product Manager with 12+ years of experience delivering secure, high-quality software and hardware solutions. Expert in aligning products with business goals, fostering cross-functional collaboration, and driving product strategy with a strong focus on customer needs and user experience.

Accomplishments

- **Enhanced Security and Compliance:** Designed and deployed a comprehensive software traceability and Software Bill of Material (SBOM) capability, bolstering Intel's security posture across all externally released software. This initiative potentially saved \$300M by addressing vulnerability management and meeting customer requirements.
- **Regulatory Compliance for Software Products:** Spearheaded the design and adoption of a unified system for storing software assets and build infrastructure, enabling Intel to meet customer support requirements and EU regulations across 250+ software products.
- **Overcame challenges in the fast-evolving AI hardware market** to lead the successful launch of Tenstorrent's second-generation AI silicon. Drove the convergence of power, performance, and area (PPA) for 7nm AI hardware, resolving critical issues and positioning Tenstorrent as a competitive player in the industry.

Core Competencies

Agile Development | User Experience | AB Testing | Product Roadmap | Leadership | Strategic Thinking | Data Analysis | Product Strategy | Prioritization | Product Marketing | Product Lifecycle Management | Cross-Functional Collaboration | Product Launch | Wireframes | Mockups

Leadership Principles

Clarity in Communication | Lead by Example | Build Trust Relationships | Innovate & Simplify

Work Experience

SENIOR SOFTWARE PRODUCT MANAGER | INTEL | 2022 – PRESENT

Pioneered the development and deployment of Intel's Long-Term Retention & Support (LTRS) solution, ensuring compliance with the EU Cyber Resilience Act and strengthening Intel's cybersecurity leadership.

- Spearheaded product execution from requirements gathering and UI design to go-to-market strategy.
- Enhanced security and support for over 250 externally released software products.
- Championed the integration of agile methodologies into the product lifecycle, fostering a culture of continuous improvement that enhanced team productivity by 20% and ensured rapid iterations based on user feedback.

Spearheaded a transformative pilot project to revolutionize Semantic Data Retrieval at Intel, poised to enhance design and validation productivity by up to 10x, driving a significant reduction in time-to-market for Intel products.

- Defined the product vision and roadmap, guiding cross-functional teams in the creation of a design knowledge graph and the integration of Generative AI (GenAI). Partnered with UX designers and engineers to shape a user-centric chatbot UI, ensuring alignment with user requirements. The result was a searchable database of over 850 specifications, enriched with semantic intelligence, enabling precise and efficient data retrieval through an intuitive UI/chatbot interface.

SOFTWARE PRODUCT MANAGER | INTEL | 2021 – 2022

Led a cross-functional team of ten people, in the design and deployment of an end-to-end software traceability and SBOM generation system, revolutionizing Intel's approach to software vulnerability management.

- Implemented a robust system to rapidly identify and mitigate software vulnerabilities, reducing tracking time by 100X.
- Facilitated requirement-gathering with key OEM clients, engaging stakeholders from DELL and HPE; integrated feedback that resulted in a 25% reduction in development cycle time for new features.
- Defined and monitored KPIs, scorecards, and dashboards to ensure high-quality BOM data and drive adoption across teams.

HARDWARE PRODUCT MANAGER | TENSTORRENT | 2020 – 2021

Led strategic planning, project management, and stakeholder communication to manage the convergence of PPA for Tenstorrent's 7nm AI hardware, culminating in the tape-out of the second-generation product.

- Cross-Functional Collaboration: Coordinated with architects, hardware designers, and validation teams to meet product timelines and enhance communication.

PHYSICAL DESIGN LEAD | INTEL | 2018 – 2020

Directed the design and optimization of Intel's 7nm microprocessor, focusing on achieving power, area, and timing targets through innovative approaches to synthesis, floorplanning, placement, and routing.

- Cross-Disciplinary Leadership: Led a team in resolving architectural and reliability challenges, driving successful project delivery through innovation and collaboration.

SCAN RTL INTEGRATION ENGINEER | INTEL | 2012 – 2018

Architected and executed Design-for-Test (DFT) solutions for multiple generations of Intel Core processors, achieving industry-leading scan insertion rates and optimizing design turnaround time.

- Achieved 99% scan insertion by addressing complex reset and clock controllability challenges.

Education

M.S IN ELECTRICAL AND COMPUTER ENGINEERING | 2010-2012
UNIVERSITY OF FLORIDA, GAINESVILLE

B.TECH IN ELECTRONICS & INSTRUMENTATION ENGINEERING | 2006-2010
WEST BENGAL UNIVERSITY OF TECHNOLOGY, INDIA

Tools

Figma | Mural | JIRA | Trello | Qualtrics | Confluence | Google Analytics | Asana | Tableau | Notion | SharePoint